

Day : Friday  
Date: 5/20/2005

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# PALM INTRANET

## Inventor Name Search Result

Your Search was:

Last Name = MOBLEY

First Name = KENNETH

Application#	Patent#	Status	Date Filed	Title	Inventor Name 34
<a href="#">60158359</a>	Not Issued	159	10/12/1999	METHOD FOR REDUCING THE WIDTH OF A GLOBAL DATA BUS	MOBLEY, KENNETH J.
<a href="#">60059869</a>	Not Issued	159	09/24/1997	SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY DEVICE INCORPORATING A STATIC RAM CACHE	MOBLEY, KENNETH J.
<a href="#">11091001</a>	Not Issued	019	03/24/2005	METHOD FOR REDUCING THE WIDTH OF A GLOBAL DATA BUS IN A MEMORY ARCHITECTURE	MOBLEY, KENNETH J.
<a href="#">10965602</a>	Not Issued	020	10/13/2004	METHOD AND CIRCUIT FOR INCREASING THE MEMORY ACCESS SPEED OF AN ENHANCED SYNCHRONOUS MEMORY	MOBLEY, KENNETH J.
<a href="#">10178072</a>	<a href="#">6813679</a>	150	06/20/2002	METHOD AND CIRCUIT FOR INCREASING THE MEMORY ACCESS SPEED OF AN ENHANCED SYNCHRONOUS SDRAM	MOBLEY, KENNETH J.
<a href="#">09962287</a>	Not Issued	041	09/24/2001	ENHANCED DRAM WITH EMBEDDED REGISTERS	MOBLEY, KENNETH J.
<a href="#">09828283</a>	Not Issued	071	04/05/2001	METHOD FOR HIDING A REFRESH IN A PSEUDO-STATIC MEMORY	MOBLEY, KENNETH J.
<a href="#">09790331</a>	Not Issued	160	02/21/2001	METHOD FOR HIDING A REFRESH IN A PSEUDO-STATIC MEMORY	MOBLEY, KENNETH J.
<a href="#">09703765</a>	<a href="#">6501698</a>	150	11/01/2000	STRUCTURE AND METHOD FOR HIDING DRAM CYCLE TIME BEHIND A BURST ACCESS	MOBLEY, KENNETH J.

<u>09689219</u>	<u>6538928</u>	150	10/11/2000	METHOD FOR REDUCING THE WIDTH OF A GLOBAL DATA BUS IN A MEMORY ARCHITECTURE	MOBLEY, KENNETH J.
<u>09626623</u>	<u>6301183</u>	150	07/27/2000	ENHANCED BUS TURNAROUND INTEGRATED CIRCUIT DYNAMIC RANDOM ACCESS MEMORY DEVICE	MOBLEY, KENNETH
<u>09533923</u>	<u>6278646</u>	150	03/23/2000	MULTI-ARRAY MEMORY DEVICE, AND ASSOCIATED METHOD, HAVING SHARED DECODER CIRCUITRY	MOBLEY, KENNETH J.
<u>09515007</u>	<u>6151236</u>	150	02/29/2000	ENHANCED BUS TURNAROUND INTEGRATED CIRCUIT DYNAMIC RANDOM ACCESS MEMORY DEVICE	MOBLEY, KENNETH
<u>09360373</u>	<u>6289413</u>	150	10/15/1999	CACHED SYNCHRONOUS DRAM ARCHITECTURE HAVING A MODE REGISTER PROGRAMMABLE CACHE POLICY	MOBLEY, KENNETH J.
<u>09236804</u>	<u>6330636</u>	150	01/29/1999	DOUBLE DATA RATE SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY DEVICE INCORPORATING A STATIC RAM CACHE PER MEMORY BANK	MOBLEY, KENNETH J.
<u>09182994</u>	<u>6347357</u>	150	10/30/1998	ENHANCED DRAM WITH EMBEDDED REGISTERS	MOBLEY, KENNETH J.
<u>09146726</u>	<u>6055192</u>	150	09/03/1998	DYNAMIC RANDOM ACCESS MEMORY WORD LINE BOOST TECHNIQUE EMPLOYING A BOOST-ON-WRITES POLICY	MOBLEY, KENNETH J.
<u>09112223</u>	Not Issued	161	07/08/1998	MEMORY DEVICE AND ASSOCIATED METHOD HAVING DEDICATED BURST ADDRESS BUS	MOBLEY, KENNETH
<u>09111822</u>	<u>6064620</u>	150	07/08/1998	MULTI-ARRAY MEMORY DEVICE, AND ASSOCIATED METHOD, HAVING SHARED DECODER CIRCUITRY	MOBLEY, KENNETH J.
<u>09069468</u>	<u>6141281</u>	150	04/29/1998	TECHNIQUE FOR REDUCING ELEMENT DISABLE FUSE PITCH REQUIREMENTS IN AN INTEGRATED CIRCUIT DEVICE INCORPORATING REPLACEABLE CIRCUIT	MOBLEY, KENNETH J.

				ELEMENTS	
<u>09023656</u>	Not Issued	161	02/09/1998	SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY DEVICE INCORPORATING A STATIC RAM CACHE	MOBLEY, KENNETH J.
<u>08888371</u>	<u>5887272</u>	150	07/03/1997	ENHANCED DRAM WITH EMBEDDED REGISTERS	MOBLEY, KENNETH J.
<u>08850802</u>	<u>5991851</u>	150	05/02/1997	ENHANCED SIGNAL PROCESSING RANDOM ACCESS MEMORY DEVICE UTILIZING A DRAM MEMORY ARRAY INTEGRATED WITH AN ASSOCIATED SRAM CACHE AND INTERNAL REFRESH CONTROL	MOBLEY, KENNETH J.
<u>08731790</u>	<u>5787457</u>	150	10/18/1996	CACHED SYNCHRONOUS DRAM ARCHITECTURE ALLOWING CONCURRENT DRAM OPERATIONS	MOBLEY, KENNETH J.
<u>08483279</u>	<u>5663915</u>	150	06/07/1995	AMPLIFIER AND METHOD FOR SENSING HAVING A PRE-BIAS OR COUPLING STEP	MOBLEY, KENNETH J.
<u>08460665</u>	<u>5721862</u>	150	06/02/1995	ENHANCED DRAM WITH SINGLE ROW SRAM CACHE FOR ALL DEVICE READ OPERATIONS	MOBLEY, KENNETH J.
<u>08319289</u>	<u>5699317</u>	150	10/06/1994	ENHANCED DRAM WITH ALL READS FROM ON-CHIP CACHE AND ALL WRITERS TO MEMORY ARRAY	MOBLEY, KENNETH J.
<u>08018802</u>	<u>5327026</u>	150	02/17/1993	SELF-TIMED BOOTSTRAP DECODER	MOBLEY, KENNETH J.
<u>08010342</u>	<u>5321324</u>	150	01/28/1993	LOW-TO-HIGH VOLTAGE TRANSLATOR WITH LATCH- UP IMMUNITY	MOBLEY, KENNETH J.
<u>07824211</u>	Not Issued	161	01/22/1992	ENHANCED DRAM WITH EMBEDDED REGISTERS	MOBLEY, KENNETH J.
<u>07644903</u>	<u>5134310</u>	150	01/23/1991	CURRENT SUPPLY CIRCUIT FOR DRIVING HIGH CAPACITANCE LOAD IN AN INTEGRATED CIRCUIT	MOBLEY, KENNETH J.
<u>07243414</u>	<u>4888733</u>	150	09/12/1988	NON-VOLATILE MEMORY CELL AND SENSING METHOD	MOBLEY, KENNETH J.

07023184	4766333	150	03/09/1987	CURRENT SENSING DIFFERENTIAL AMPLIFIER	MOBLEY, KENNETH J.
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Inventor Search Completed: No Records to Display.

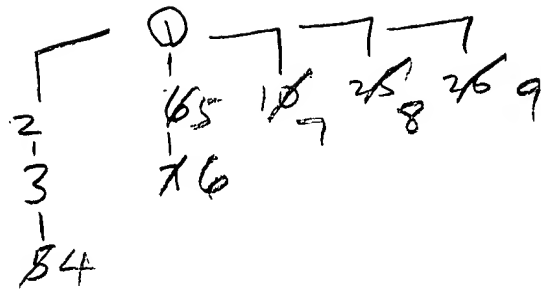
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<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>	<input type="button" value="Search"/>
	MOBLEY	KENNETH	

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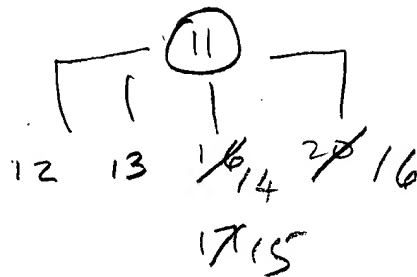
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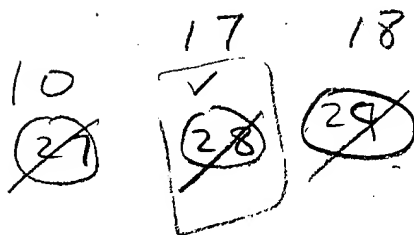
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